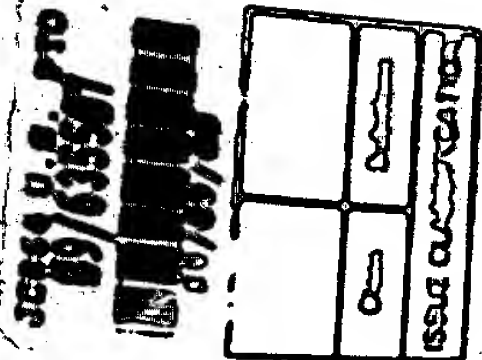


3



PATENT NUMBER

## U.S. UTILITY Patent Application

PT3	O.I.P.E.	PATENT DATE
SCANNED <i>Am'</i>	Q.A. <i>A.G.</i>	

APPLICATION NO. 09/635507	CONT/PRIOR	CLASS 438	SUBCLASS 712	ART UNIT 1765	EXAMINER <i>Anderson</i>
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APPLICANTS  
Krishnaswamy Ramkumar  
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Venuka JayatilakaTITLE  
Process for reducing leakage in an integrated circuit with shallow trench isolated active areasPTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL		CROSS-REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.			<b>NOTICE OF ALLOWANCE MAILED</b>	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____			<b>ISSUE FEE</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<input type="checkbox"/> (Assistant Examiner) _____ (Date) _____			Amount Due	Date Paid
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